

To, 13  
the principal,  
MSRMCE,  
Kadapa.

Kadapa  
DT: 22/10/2021

Respected Sir,

Sub: Request to grant permission for conducting  
Three day workshop on "Design of Electrical circuits using  
MULTISIM". - Regd.

The department of EEE is going to organise a Three  
day workshop on "Design of Electrical circuits using  
MULTISIM" from 28/10/2021 to 30/10/2021. The  
resource persons are Smt. Saleha Tabassum and  
Miss. A. Jyothirmayi, Assistant professors EEE department

In this regard, I request you to give permission  
for conducting the above said event. Further I  
request to sanction an advance amount of Rs 3000/-.

Thanking you Sir,

Yours faithfully,  
Saleha Tabassum,  
Asst. Prof.  
EEE-Dept.

Forwarded to  
principal sir  
Kindly consider & grant  
permission to conduct  
workshop.

Jyothirmayi  
22/10/21

10

AM.

Permitted and  
Advance may be  
given MM



# K.S.R.M. COLLEGE OF ENGINEERING

(UGC-AUTONOMOUS)

Kadapa, Andhra Pradesh, India- 516 005

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Department of Electrical & Electronics Engineering

Date: 15.10.21

## CIRCULAR

This is to inform all the B. Tech., III Semester students that department is going to conduct Three Day Workshop. In this regard, I request the students those who are interested for the workshop can register their names with the concern coordinator. The schedule of the course is as follows.

Name of the Workshop	:	Design of Electrical Circuits using MULTISIM
Resource Person (S)	:	Mrs. Saleha Tabassum, Assistant Professor Ms. A. Jyothirmayi, Assistant Professor
Workshop Duration	:	18Hrs
Date (S) of the Workshop	:	28.10.21 to 30.10.21
Timings	:	9.00am to 4.00pm
Venue	:	SJ 114 – Simulation Lab
Faculty Coordinator	:	Ms. A. Jyothirmayi, Assistant Professor

*Amresh* 15/10/21

Dr. K. AMARESH

Head of the Department



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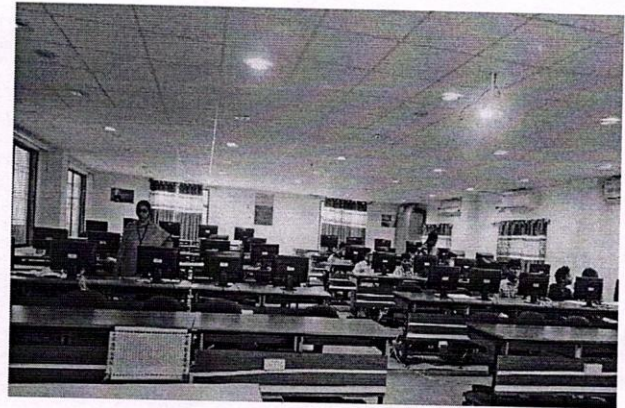
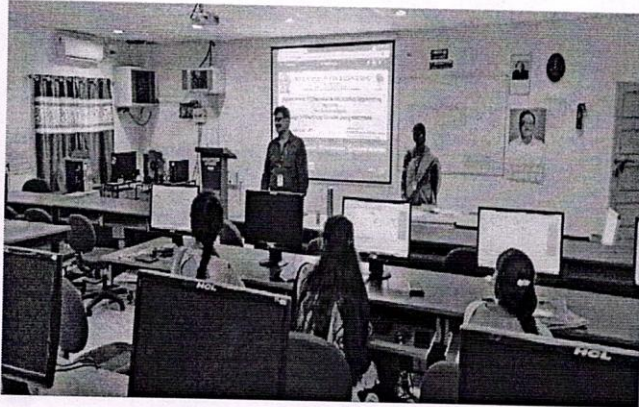


## Department of Electrical & Electronics Engineering Activity Report

Name of the Activity	: Three Day Workshop
Title of the Workshop	: Design of Electrical Circuits Using MULTISIM
Date(s) of the Workshop	: 30.10.2021 to 02.11.2021
Scheduled Time	: 09.00 am to 4:00 pm
Resource Person(s)	: Mrs. Saleha Tabassum & Ms. A. Jyothirmayi
Target Audience	: III Semester Students
No. of Students Attended	: 25
Co-coordinator(s)	: Mrs. Saleha Tabassum & Ms. A. Jyothirmayi
Venue of the Event	: SJ-115 (Simulation Lab)

### Activity Description:


MULTISIM is one of the important tools for electrical students to learn the circuit designing. Department of EEE organized a Workshop on "Design of Electrical Circuits Using MULTISIM". Head of the Department, faculty & participants of the workshop inaugurated the workshop with all good spirit. Resource persons began the first day first session Introduction to MULTISIM followed by Measurement of electrical parameters in the afternoon session. Second day first session is Implementation of Network Solving Techniques; Afternoon session is Sinusoidal Network Analysis. Third day first session is Application of Electrical Concepts ( Glow of LED, Regulated Power Supply Circuit, buzzer circuit etc), followed by afternoon session exercises and finally valedictory. Students were issued participation certificates by the Head of the Department.



*Jyothirmayi*  
*Ajitha*  
Coordinator(S)

*Jyothirmayi*  
Head of the Department

*V. S. S. Murthy*  
Principal

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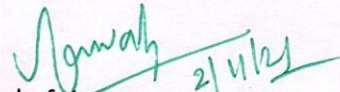
Department of Electrical & Electronics Engineering

Three Day Workshop on "DESIGN OF ELECTRICAL CIRCUITS USING MULTISIM"

List of Participants

S. No.	Roll No.	Name	Signature of the Students		
			30.10.21	01.11.21	02.11.21
1	209Y1A0204	B. Neeraja Reddy	B. Neeraja Reddy	B. Neeraja Reddy	B. Neeraja Reddy
2	209Y1A0205	B. Yaswanth	B. Yaswanth	B. Yaswanth	B. Yaswanth
3	209Y1A0207	D. Sai pavan	Sai pavan	Sai pavan	Sai pavan
4	209Y1A0208	D.V.N. Ruchitha	D. Ruchitha	D. Ruchitha	D. Ruchitha
5	209Y1A0209	D. Dharani	D. Dharani	D. Dharani	D. Dharani
6	209Y1A0210	D. Tejaswini	D. Tejaswini	D. Tejaswini	D. Tejaswini
7	209Y1A0214	G. Siva Kumar	G. siva kumar	G. siva kumar	G. siva kumar
8	209Y1A0216	K. Sai Rahul	K. Sai Rahul	K. Sai Rahul	K. Sai Rahul
9	209Y1A0317	K. Likhitha	K. Likhitha	K. Likhitha	K. Likhitha
10	209Y1A0219	K. Mohan Krishan	K. Mohan	K. Mohan	K. Mohan
11	209Y1A0222	L. N. Sai Pavan	Sai pavan	Sai pavan	Sai pavan
12	209Y1A0223	M. Gowri	M. Gowri	M. Gowri	M. Gowri
13	209Y1A0225	M. C. Bharath Kumar Reddy	M. C. Bharath	M. C. Bharath	M. C. Bharath
14	209Y1A0227	M. Maha Lakshmi	M. Maha Lakshmi	M. Maha Lakshmi	M. Maha Lakshmi
15	209Y1A0229	N. Sasi Rekha	N. Sasi Rekha	N. Sasi Rekha	N. Sasi Rekha
16	209Y1A0231	P. Yogna	P. Yogna	P. Yogna	P. Yogna
17	209Y1A0233	P. Ravi Shankar	P. Ravi Shankar	P. Ravi Shankar	P. Ravi Shankar
18	209Y1A0235	P. Sai Vignesh	Sae vignesh	Sae vignesh	Sae vignesh
19	209Y1A0237	R. Thirumala	R. Thirumala	R. Thirumala	R. Thirumala
20	209Y1A0240	S. Yuvaraj	S. Yuvaraj	S. Yuvaraj	S. Yuvaraj
21	209Y1A0241	S. Alisha Sameera	Alisha	Alisha	Alisha
22	209Y1A0243	S. Mohd. Javeed	S. Javeed	S. Javeed	S. Javeed
23	209Y1A0247	S. Shashikala	S. Shashikala	S. Shashikala	S. Shashikala
24	209Y1A0249	T. Pallavi	T. Pallavi	T. Pallavi	T. Pallavi
25	209Y1A0252	V. Jagadeesh	V. Jagadeesh	V. Jagadeesh	V. Jagadeesh

  
Coordinator(S)

  
Head of the Department



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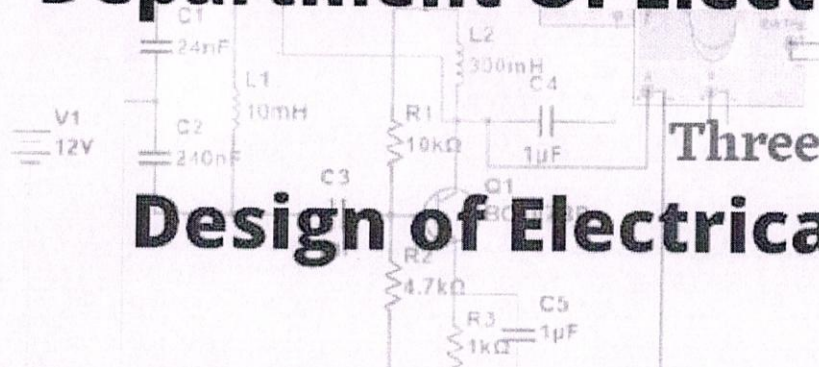
**KSNR**  
lives on..

## Department Of Electrical & Electronics Engineering

Organizing

Three Days Workshop on

## Design of Electrical Circuits using MULTISIM



**DATES : 28-10-2021 TO 30-10-2021**

**VENUE : SJ 112**

**Resource Persons**

**Mrs.Saleha Tabassum,Asst.Professor**

**Ms.A.Jyothirmayi,Asst.Professor**

**Dr.K.Amaresh**  
HOD

**Dr.V.S.S.Murthy**  
Principal

**Prof.A.Mohan**  
Director

**Dr.K.Chandra Obul Reddy**  
Managing Director

**Smt.K.Rajeswari**  
Correspondent

**Sri K.Madan Mohan Reddy**  
Vice-Chairman

**Sri K.Raja Mohan Reddy**  
Chairman



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